

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re the Application of: YONEDA, et al.

Serial Number: 10/650,692

Application Division

Filed: August 29, 2003

Customer No.:38834

For: MANUFACTURING METHOD OF A SEMICONDUCTOR DEVICE INCORPORATING PASSIVE ELEMENT AND REDISTRIBUTIN BOARD

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Date: March 1, 2004

Sir:

Please supply the undersigned attorney with a corrected filing receipt for the above-identified application. The undersigned also respectfully requests that the Patent and Trademark Office records be amended to reflect the correction.

In reviewing the Official Filing Receipt, we noted an error in the Applicants. The fourth and the sixth to the twelfth applicants should be removed. A copy of Petition Under 37 C.F.R. § 1.48(b) In Support of Amendment Correcting Inventorship is enclosed which indicates the correct information. We are enclosing a copy of the filing receipt with the corrections highlighted.

In the event that any fees are required in connection with this paper, please charge our Deposit Account No. <u>50-2866</u>.

Respectfully submitted,

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G. Adrian

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Enclosures:

Official Filing Receipt;

Petition Under 37 C.F.R. § 1.48(b) In Support of Amendment Correcting Inventorship

FILING OR 371

TENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE UNITED STATES DEPARTMENT OF COMMI United States Patent and Trademark Office Address COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandris, Viginia 22313-1450 www.usplu.gov

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ATTY.DOCKET NO DRAWINGS TOT CLMS IND CLMS

(c) DATE 10/650,692 08/29/2003

APPL NO.

ART UNIT 2812

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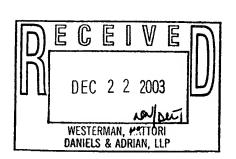
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Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

FUJITSU LIMITED, Kawasaki, JAPAN;

Domestic Priority data as claimed by applicant

This application is a DIV of 10/100,011 03/19/2002

Foreign Applications

JAPAN 2001-335413 10/31/2001

If Required, Foreign Filing License Granted: 12/09/2003

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Projected Publication Date: 03/18/2004

Non-Publication Request: No

Early Publication Request: No

Title

Manufacturing method of a semiconductor device incorporating a passive element and a redistribution board

Preliminary Class

438

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